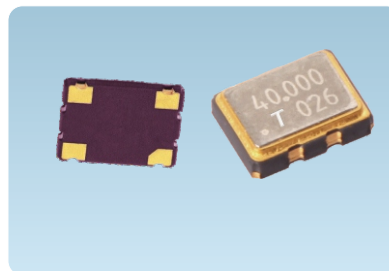


T07-3 TYPE

Typical 7.0 × 5.0 × 1.3 mm

Low jitter



Feature

- Typical 7.0 × 5.0 × 1.3 mm ceramic SMD package.
- Tight symmetry(45 to 55%) available.
- Packing:Tape & Reel,1000 pcs per Reel.

Typical Application

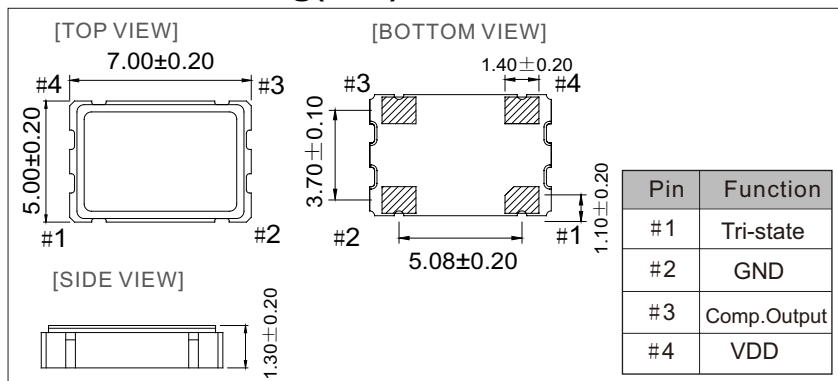
- xDSL,WLAN,Fiber/10G-Bit Ethernet
- Notebook,PDA
- PC main board,VGA card

Specifications

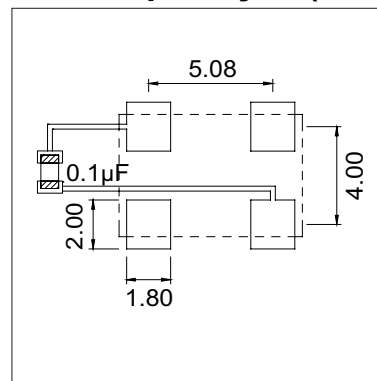
Parameter	3.3V		2.5V		1.8V		Unit
	Min	Max	Min	Max	Min	Max	
Supply Voltage Variation(VDD)	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	2.048	200	2.048	166	2.048	110	MHz
Supply Current							
1 MHz ≤ Fo ≤ 110MHz	–	20	–	15	–	10	mA
110 MHz < Fo ≤ 166MHz	–	25	–	20	–	–	
166 MHz < Fo ≤ 200 MHz	–	30	–	–	–	–	
Duty Cycle	40	60	40	60	40	60	%
Output Level(CMOS)							
Output High(Logic"1")	2.97	–	2.25	–	1.62	–	V
Output Low(Logic"0")	–	0.33	–	0.25	–	0.18	
Transition Time;Rise/Fall Time+							
1 MHz ≤ Fo ≤ 200 MHz	–	5	–	5	–	5	nSec
Period Jitter (Pk-Pk)							
Specific Frequency"	–	40	–	40	–	40	pSec
Others	–	200	–	200	–	200	
Start Time	–	5	–	5	–	5	mSec
Tri-State(Input to Pin 1)							
Enable (High voltage or floating)	2.31	–	1.75	–	1.26	–	V
Disable (Low voltage or GND)	–	0.99	–	0.75	–	0.54	
Standby Current	–	15	–	15	–	15	µA
Aging (@25°C 1st year)	–	±3	–	±3	–	±3	ppm
Storage Temp. Range	- 55	125	- 55	125	- 55	125	°C

+Transition times are measures between 10% and 90% of VDD,With an output load of 15pF.

Outline Drawing(mm)



Solder pad layout(mm)



Frequency Stability Vs. Temperature Range

Temp.(°C)	Ppm	±20	±25	±50
-10 ~ +60		✓	✓	✓
-20 ~ +70		+	✓	✓
-40 ~ +85		+	✓	✓

✓ Available + Conditional × Not Available

Inclusive of calibration @25°C ,operating temperature range, input voltage variation,load variation,aging(1st year), shock,and vibration