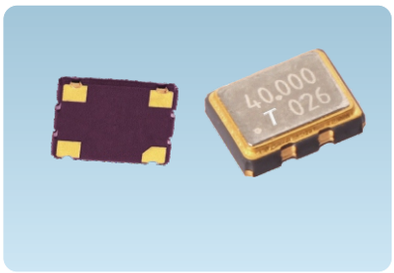
Crystal Oscillator

RoHS compliance

TO7 TYPE

Typical 7.0 × 5.0 × 1.3 mm

Low jitter



Feature

- Typical 7.0 × 5.0 × 1.3 mm ceramic SMD package.
- Tight symmetry(45 to 55%) available.
- Packing:Tape & Reel,1000 pcs per Reel.

Typical Application

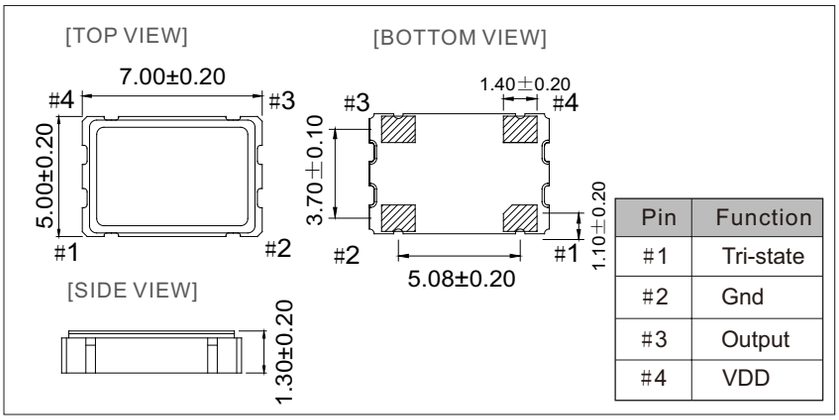
- xDSL,WLAN,Fiber/10G-Bit Ethernet
- Notebook,PDA
- PC main board,VGA card

Specifications

Parameter	3.3V		2.5V		1.8V		Unit
	Min	Max	Min	Max	Min	Max	
Supply Voltage Variation(VDD)	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	0.0137	166	0.0137	133	0.0137	125	MHz
Supply Current							
13.7 Khz ≤ Fo < 70KHz	–	1	–	1	–	1	mA
0.3125 Mhz ≤ Fo < 35.328MHz(A1)	–	10	–	8	–	7	
30 Mhz ≤ Fo < 75 MHz	–	20	–	18	–	15	
75 Mhz ≤ Fo < 133 MHz	–	35	–	30	–	25	
133 Mhz ≤ Fo	–	45	–	40	–	–	
Output Level(CMOS)							
Output High(Logic"1")	90%V_{DD}	–	90%V_{DD}	–	90%V_{DD}	–	V
Output Low(Logic"0")	–	10%V_{DD}	–	10%V_{DD}	–	10%V_{DD}	
Transition Time;Rise/Fall Time+							
13.7 Mhz ≤ Fo <70 MHz	50Max.						nSec
0.3125 MHz ≤ Fo <100 MHZ	5Max.						
100 MHz ≤ Fo	3Max.						
Start Time	5Max.						msec
Tri-State(Input to Pin 1)							V
Enable(High voltage floating)	0.7V_{DD} Min.						
Disable(Low voltage or GND)	03V_{DD} Max.						
RMS Phase Jitter (Integated 12K~20MHz)	1Max.						pSec
Standby Current	10Max.						μA
Aging (@25℃ 1st year)	±3Max.						ppm
Storage Temp. Range	-55~125						°C

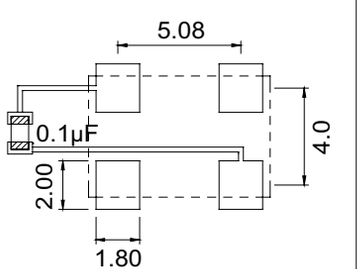
+Transition times are measures between 10% and 90% of VDD,With an output load of 15pF.

Outline Drawing(mm)



Pin	Function
#1	Tri-state
#2	Gnd
#3	Output
#4	VDD

Solder pad layout(mm)



Frequency Stability Vs. Temperature Range

Temp.(℃) \ Ppm	±20	±25	±50
-10 ~ +60	✓	✓	✓
-20 ~ +70	+	✓	✓
-40 ~ +85	+	✓	✓
-40 ~ +125	×	×	✓

✓ Available + Conditional × Not Available

Inclusive of calibration @25℃,operating temperature range, input voltage variation,load variation,aging(1st year), shock,and vibration