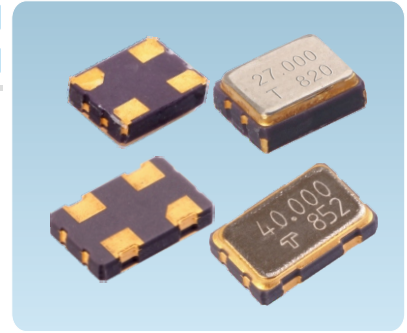


### T05 TYPE

Typical 5.0×3.2×1.2mm

Low standby current



#### Feature

- Typical 5.0 × 3.2 × 1.2 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Packing: Tape & Reel, 1000 pcs per Reel.
- Low phase jitter. Operation voltage: 1. 8V, 2. 5V, 3. 3V.

#### Typical Application

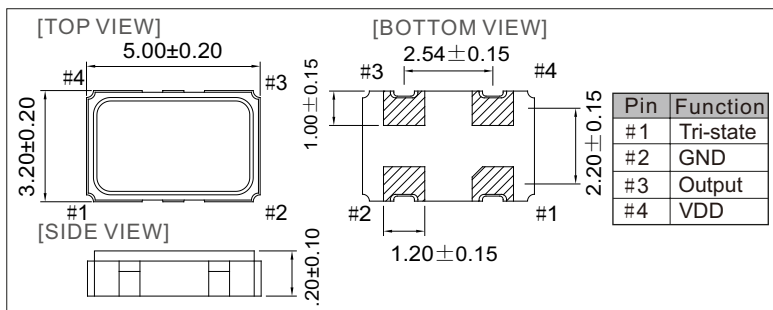
- Mobile Phone; WLAN, Wireless; Fiber/10G-Bit Ethernet; Notebook, Pad

#### Specifications

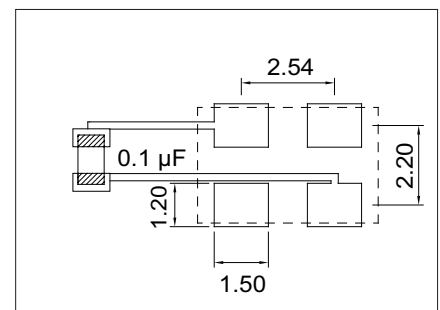
Parameter	3.3V		2.5V		1.8V		Unit
	Min	Max	Min	Max	Min	Max	
Supply Voltage Variation(VDD)	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	0.0137	160	0.0137	160	0.0137	135	MHz
Supply Current							
13.7KHz≤Fo≤93KHz	–	1	–	1	–	1	mA
0.3125MHz≤Fo < 50MHz(A1)	–	10	–	8	–	7	
40MHz≤Fo < 75MHz	–	20	–	18	–	15	
75MHz≤Fo < 135MHz	–	35	–	30	–	25	
135MHz≤Fo	–	45	–	40	–	–	
Duty Cycle	45	55	45	55	45	55	%
Output Level(CMOS)							
Output High(Logic"1")	90% V <sub>DD</sub>	–	90% V <sub>DD</sub>	–	90% V <sub>DD</sub>	–	V
Output Low(Logic"0")	–	10% V <sub>DD</sub>	–	10% V <sub>DD</sub>	–	10% V <sub>DD</sub>	
Transition Time:Rise/Fall Time							
13.7kHz≤Fo≤93kHz	–	50	–	50	–	50	nSec
0.3125MHz≤Fo < 100MHz	–	5	–	5	–	5	
100MHz≤Fo	–	3	–	3	–	3	
Start Time	5Max.						mSec
Tri-State(Input to Pin 1)							
Enable(High voltage floating)	0.7 V <sub>DD</sub> Min.						V
Disable(Low voltage or GND)	0.3 V <sub>DD</sub> Max.						
RMS Phase Jitter (Integrated 12K~20MHz)	1Max.						pSec
Standby Current	10Max.						μA
Aging (@25°C 1st year)	±3Max.						ppm
Storage Temp. Range	-55~125						°C

+Transition times are measures between 10% and 90% of VDD, With an output load of 15pF.

#### Outline Drawing(mm)



#### Solder pad layout(mm)



#### Frequency Stability Vs. Temperature Range

Temp.(°C)	Ppm	±20	±25	±50
-10 ~ +60	✓	✓	✓	✓
-20 ~ +70	+	✓	✓	✓
-40 ~ +85	+	✓	✓	✓
-40 ~ +125	×	×	×	✓

Inclusive of calibration @25°C, operating temperature range, input voltage variation, load variation, aging(1<sup>st</sup> year), shock, and vibration

✓ Available + Conditional × Not Available