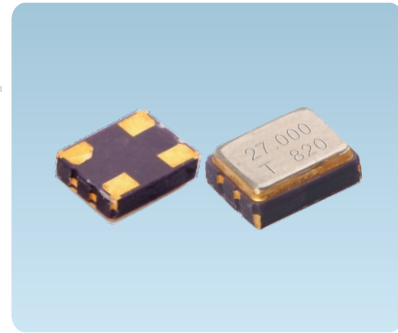


T03 TYPE

Typical 3.2×2.5×0.95mm

Low jitter



Feature

- Typical 3.2 × 2.5 × 0.95 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Packing: Tape & Reel, 3000 pcs per Reel.
- Low phase jitter. Operation voltage: 1.8V, 2.5V, 3.3V.

Typical Application

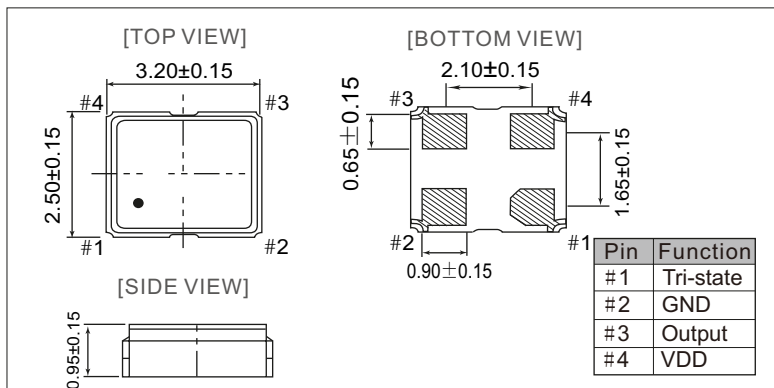
- Mobile Phone; WLAN, Wireless; Fiber/10G-Bit Ethernet; Notebook, Pad

Specifications

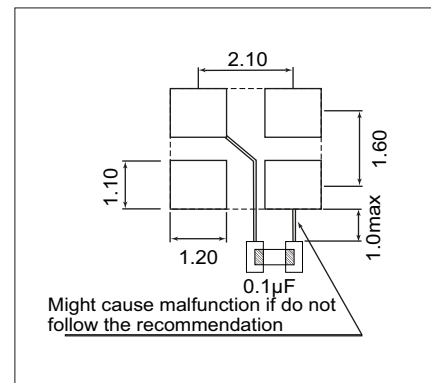
Parameter	3.3V		2.5V		1.8V		Unit
	Min	Max	Min	Max	Min	Max	
Supply Voltage Variation(VDD)	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	0.032768	125	0.032768	125	0.032768	125	MHz
Supply Current $F_o = 32.768\text{KHz} (@15\text{pF load})$	–	70	–	66	–	63	μA
1.25MHz $\leq F_o < 100\text{MHz}$	–	15	–	10	–	7	mA
100MHz $\leq F_o \leq 125\text{MHz}$	–	25	–	20	–	12	
Duty Cycle	45	55	45	55	45	55	%
Output Level(CMOS)							V
Output High(Logic"1")	90% V _{DD}	–	90% V _{DD}	–	90% V _{DD}	–	
Output Low(Logic"0")	–	10% V _{DD}	–	10% V _{DD}	–	10% V _{DD}	
Transition Time; Rise/Fall Time+							nSec
$F_o = 32.768\text{KHz}$	50Max.						
1.25MHz $\leq F_o < 20\text{MHz}$	4Max.			5Max.			
20MHz $\leq F_o \leq 125\text{MHz}$	3Max.			4Max.			
Start Time	2Max.						mSec
Tri-State(Input to Pin 1)							V
Enable(High voltage floating)	0.7 V _{DD} Min.						
Disable(Low voltage or GND)	0.3 V _{DD} Max.						
RMS Phase Jitter (Integrated 12K~20MHz)	1Max.						pSec
Standby Current	10Max.						μA
Aging (@25°C 1st year)	$\pm 3\text{Max.}$						ppm
Storage Temp. Range	-55~125						°C

+Transition times are measures between 10% and 90% of VDD, With an output load of 15pF.

Outline Drawing(mm)



Solder pad layout(mm)



Frequency Stability Vs. Temperature Range

Temp.(°C)	Ppm	±20	±25	±50
-10 ~ +60	✓	✓	✓	✓
-20 ~ +70	+	✓	✓	✓
-40 ~ +85	×	✓	✓	✓
-40 ~ +125	×	×	×	✓

✓ Available + Conditional × Not Available

Inclusive of calibration @25°C, operating temperature range, input voltage variation, load variation, aging(1st year), shock, and vibration