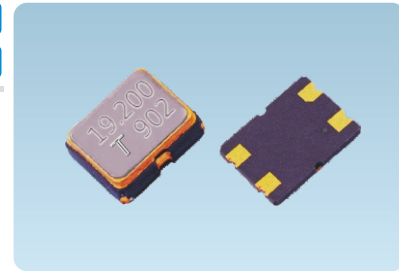


T01 TYPE

Typical 2.05 × 1.65 × 0.75 mm

Low phase jitter



Crystal Oscillator

Feature

- Typical 2.05 × 1.65 × 0.75 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Low phase jitter.
- Operation voltage: 1.8V, 2.5V, 3.3V.
- Packing: Tape & Reel, 3000 pcs per Reel.

Typical Application

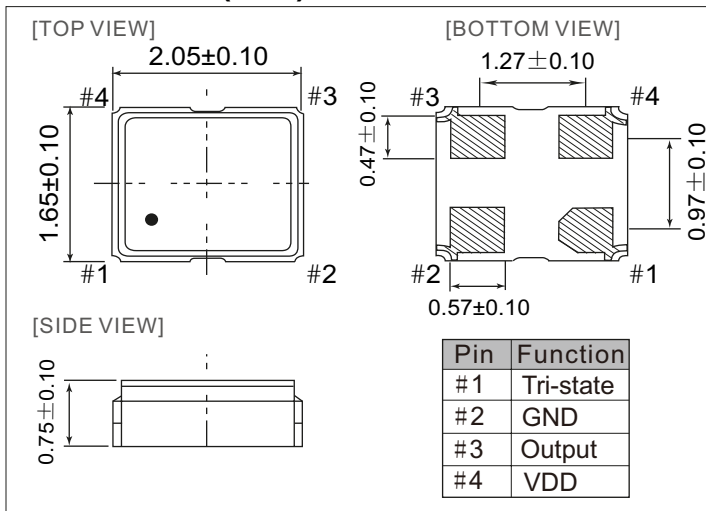
- WLAN/WiMax
- DSC, Set - top Box, HDTV
- Mobile Phone

Specifications

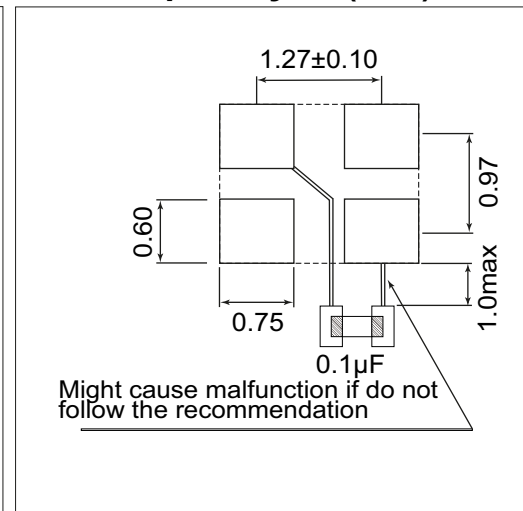
Parameter	3.3V		2.5V		1.8V		Unit
	Min	Max	Min	Max	Min	Max	
Supply Voltage Variation(VDD)	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	1.5	50	1.5	50	1.5	50	MHz
Supply Current	-	15	-	10	-	7	mA
Duty Cycle	45	55	45	55	45	55	%
Output Level(CMOS)							
Output High(Logic"1")	90% V _{DD}	-	90% V _{DD}	-	90% V _{DD}	-	V
Output Low(Logic"0")	-	10% V _{DD}	-	10% V _{DD}	-	10% V _{DD}	
Transition Time; Rise/Fall Time+							
1 MHz Fo < 20MHz		4		4		5	nSec
20MHz ≤ Fo < 50MHz		3		3		4	
Start Time	2Max.						mSec
Tri-State(Input to Pin 1)							
Enable(High voltage floating)	0.7 V _{DD} Min.						V
Disable(Low voltage or GND)	0.3 V _{DD} Max.						
RMS Phase Jitter (Integrated 12K~20MHz)	1Max.						pSec
Standby Current	10Max.						μA
Aging (@25°C 1st year)	±3Max.						ppm
Storage Temp. Range	-55~125						°C

+Transition times are measures between 10% and 90% of VDD, With an output load of 15pF.

Dimension(mm)



Solder pad layout(mm)



Q.C
PASS

T1903A

Frequency Stability Vs. Temperature Range

Temp.(°C)	Ppm	±20	±25	±50
-10 ~ +60	✓	✓	✓	✓
-20 ~ +70	+	✓	✓	✓
-40 ~ +85	×	+	✓	✓
-40 ~ +125	×	×	✓	✓

Inclusive of calibration @25°C, operating temperature range, input voltage variation, load variation, aging(1st year), shock, and vibration

✓ Available + Conditional × Not Available